

STM MODULE ADDRESSING

This document can be found at the following location
 \\Ppdserver\EE.D.PPD\Project\CDMS\module\9u\STM

UNUSED			Subrack SLOT				SECTION				SUBSECTION				
15	14	13	12	11	10	9	8	7	6	5	4	3	2	1	0

Available for possible future addition

SECTIONS

F	SPARE
E	SPARE
D	SPARE
C	SPARE
B	SPARE
A	SPARE
9	SPARE
8	SPARE
7	SPARE
6	SPARE
5	SPARE
4	
3	PHONON Delay Time
2	PHONON Rise Time
1	Pulse Amplitude
0	MODULE INFO/ EVENT INFO.

SUBSECTIONS

CMD/STATUS REGISTER				
	Test Point Configuration			
	Sine Wave Mode	PULSE AMPLITUDE PHONON D	RISE TIME PHONON D	DELAY TIME PHONON D
	Sequencer Mode	PULSE AMPLITUDE PHONON C	RISE TIME PHONON C	DELAY TIME PHONON C
		PULSE AMPLITUDE PHONON B	RISE TIME PHONON B	DELAY TIME PHONON B
		PULSE AMPLITUDE PHONON A	RISE TIME PHONON A	DELAY TIME PHONON A
	ID	PULSE AMPLITUDE Q AMP INNER	PULSE AMPLITUDE Q AMP OUTER	

Notes:

- For addressing purposes, modules are divided into 16 sections, each having a 4 bit address. Each section is divided into 16 subsections. For example, in order to address the Offset Dac of a RTF Module plugged into Subrack-SLOT 7, you would use address xx0011100110001; or hexadecimal address 07FE

COMMAND REGISTER

LAST POINT CONFIGURATION

Address: xx0E write mode

TEST POINT
CONFIGURATION

LINE WAVE MODE

ess: xx0D write mode

SINE WAVE
nINT/EXT nOFF/ON

QUENCER MODE

ress: xx0C write mode

Sequencer
MODE MODE
1 1

STATUS REGISTER

address: xx0F read mode

USY	ERROR				Sequencer MODE 1	MODE 1			SINE nINT/EXT	WAVE nOFF/ON		TEST POINT CONFIGURATION
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ERROR REGISTER

CONFIGURATION TIME OUT

SEQUENCER

SINE WAVE

CONTINUOUS	External Sine Wave On
SINGLE	Sine Wave OFF
SYNCH	Internal Sine Wave On
RUN	Sine Wave OFF

Test Point Configuration Signal Assignment

Sine Wave/FET Htr
Q Bias/LED
QET Bias
Squid Bias
Internal Phonon Pulser
Q Amp Output/Pulser
Squid Driver Offset
Squid Driver Output